

A 6502 Disassembler from Apple

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DESCRIPTION

This subroutine package is used to display single or sequential 6502 instructions in mnemonic form. The subroutines are tailored to disassemblers and debugging aids, but tables with more general usage (assemblers) are included. The subroutines occupy one page (256 bytes) and tables most of another. Seven page zero locations are used.

FEATURES

Four output fields are generated for each disassembled instruction: 1) Address of instruction, in hexadecimal (hex); 2) Hex code listing of instruction, 1 to 3 bytes; 3) 3-character mnemonic, or "???" for invalid ops (which assume a length of 1 byte); and 4) Address field, in one of the following formats.

Format	Address Mode
(empty)	Invalid, Implied, Accumulator
\$12	Page zero
\$1234	Absolute, Branch (<i>target</i> printed)
#\$12	Immediate
\$12.X	Zero page, indexed by X
\$12.Y	Zero page, indexed by Y
\$1234.X	Absolute, indexed by X
\$1234.Y	Absolute, indexed by Y
(\$1234)	Indirect
(\$12.X)	Indexed Indirect
(\$12).Y	Indirect Indexed

Note that unlike MOS TECHNOLOGY assemblers, which use "A" for accumulator addressing, the APPLE disassembler outputs an empty field to avoid confusion and facilitate byte counting.

USAGE

The following subroutine entries are useful.

DSMBL	Disassembles and displays 20 sequential instructions beginning at the address specified by the page zero variables PCL and PCH. For example, if called with \$D2 in PCL and \$38 in PCH, 20 instructions beginning at address \$38D2 will be disassembled. PCL and PCH are updated to contain the address of the last disassembled instruction. Must be called with 6502 in hexadecimal mode ('D' status bit clear). All processor registers are altered (except S—stack pointer). Uses INSTDSP and PCADJ.
INSTDSP	Disassembles and displays a single instruction whose address is specified by PCL and PCH. Must be called in hexadecimal mode. All processor registers (except S) are altered. Uses PCADJ3, PRPC, PRBLNK, PRBL2, PRNTAX, PRBYTE, and CHAROUT.
PRPC	Outputs a carriage return, 4 hex digits corresponding to PCH and PCL, a dash, and 3 blanks. Alters A, clears X. Uses PRNTAX and CHAROUT.
PRNTX	Outputs the contents of X as two hex digits. Alters A. Uses CHAROUT.
PRNTAX	Outputs two hex digits for the contents of A,

then two hex digits for the contents of X. A is altered. Uses CHAROUT.

PRNTYX	Same as PRNTAX except that Y and X are output. Alters A. Uses CHAROUT.
PRBLNK	Outputs 3 blanks. Alters A, clears X. Uses CHAROUT.
PRBL2	Outputs the number of blanks specified by the contents of X (0 for 256 blanks). Alters A, clears X. Uses CHAROUT.
PRBL3	Outputs a character from the A register followed by X-1 blanks. In other words, X specifies the total number of characters output. (0 for 256 blanks). Alters A, clears X. Uses CHAROUT.
PCADJ	(PCL,PCH) + 1 + (contents of page zero variable LENGTH) → Y & A (low order byte in Y). For example, if PCL = \$D2, PCH = \$38, and LENGTH = 1 (corresponding to a 2 byte instruction), PCADJ will leave Y = \$D4 and A = \$38. X is always loaded with PCH.
PCADJ2	Same as PCADJ except that A is used in place of LENGTH.
PCADJ3	Same as PCADJ2 except that the increment (+1) is specified by the carry (set = +1, clear = +0).

RUNNING AS A PROGRAM

The following program will run a disassembly.

Supplied on APPLE-1 { 9F0 200 8 JSR DSMBL
 cassette tapes. { 9F3 4C1FFF JMP MONITOR

First, put the starting address of code you want disassembled in PCL (low order byte) and PCH (high order byte). Then type 9F0 R CR (on APPLE-1 system). 20 instructions will be disassembled. Hitting R CR again will give the next 20, etc.

Cassette tapes supplied for the ACI-1 (APPLE Cassette Interface) are intended to be loaded from \$800 to \$9FF.

NON-APPLE SYSTEMS

Source and object code supplied occupies pages 8 and 9. All code is on page 8, tables are on page 9. These tables may be relocated at will: MODE, MODE2, CHAR1, CHAR2, MNEM1, and MNEMR. The code may also be relocated. Be careful if you use pages 0 or 1. Page 1 is the subroutine return stack and page 0 must contain 7 variables (to use DSMBL). These may be relocated on page 0 but PCL must always immediately precede PCH for (Z-page), Y addressing.

	\$40	FORMAT	Used
	\$41	LENGTH	} by
locations	\$42	LMNEM	} INSTDSP,
used			
by	{ \$43	RMNEM	DSMBL
supplied	\$44	PCL	} Used by PCADJ,
code	\$45	PCH	} INSTDSP, DSMBL
	\$46	COUNT	} Used by DSMBL only

MODIFICATIONS

- To change '#' to '=' for immediate mode change location \$955 (on code enclosed) from a \$A3 to a \$BD.
- To skip the '\$' (meaning hex) preceding disassembled values make the following changes:

946: 01 (was 81)
 947: 02 (was 82)
 94C: 11 (was 91)
 94D: 12 (was 92)
 94E: 06 (was 86)
 950: 05 (was 85)
 951: 1D (was 9D)
 95B: 00 (was A4)
 95C: 00 (was A4)

c) To have address field of accumulator-addressed instructions print as 'A'.

- 1) Must skip \$ preceding disassembled values by making modification b) above.
- 2) Change the following locations:
 949: 80 (was 00)
 957: C1 (was A4)

d) To add ROR and addressing modes, change the following locations:

991: 9C (was 00)
 9D1: 26 (was 00)
 919: 02 (was 00)
 91A: 45 (was 40)
 91B: B3 (was B0)
 91D: 08 (was 00)
 91F: 09 (was 00)

Address	Hex	Label	Comment
0847	00	T00	* OP CODE TO A AGAIN.
0848	00	L07	
0849	00	CPX	
084A	00	BEQ	
084B	00	LSP	
084C	00	BCC	
084D	00	BNDX3	
084E	00	BNDX3	
084F	00	BNDX3	
0850	00	BNDX3	
0851	00	BNDX3	
0852	00	BNDX3	
0853	00	BNDX3	
0854	00	BNDX3	
0855	00	BNDX3	
0856	00	BNDX3	
0857	00	BNDX3	
0858	00	BNDX3	
0859	00	BNDX3	
085A	00	BNDX3	
085B	00	BNDX3	
085C	00	BNDX3	
085D	00	BNDX3	
085E	00	BNDX3	
085F	00	BNDX3	
0860	00	BNDX3	
0861	00	BNDX3	
0862	00	BNDX3	
0863	00	BNDX3	
0864	00	BNDX3	
0865	00	BNDX3	
0866	00	BNDX3	
0867	00	BNDX3	
0868	00	BNDX3	
0869	00	BNDX3	
086A	00	BNDX3	
086B	00	BNDX3	
086C	00	BNDX3	
086D	00	BNDX3	
086E	00	BNDX3	
086F	00	BNDX3	
0870	00	BNDX3	
0871	00	BNDX3	
0872	00	BNDX3	
0873	00	BNDX3	
0874	00	BNDX3	
0875	00	BNDX3	
0876	00	BNDX3	
0877	00	BNDX3	
0878	00	BNDX3	
0879	00	BNDX3	
087A	00	BNDX3	
087B	00	BNDX3	
087C	00	BNDX3	
087D	00	BNDX3	
087E	00	BNDX3	
087F	00	BNDX3	
0880	00	BNDX3	
0881	00	BNDX3	
0882	00	BNDX3	
0883	00	BNDX3	
0884	00	BNDX3	
0885	00	BNDX3	
0886	00	BNDX3	
0887	00	BNDX3	
0888	00	BNDX3	
0889	00	BNDX3	
088A	00	BNDX3	
088B	00	BNDX3	
088C	00	BNDX3	
088D	00	BNDX3	
088E	00	BNDX3	
088F	00	BNDX3	
0890	00	BNDX3	
0891	00	BNDX3	
0892	00	BNDX3	
0893	00	BNDX3	
0894	00	BNDX3	
0895	00	BNDX3	
0896	00	BNDX3	
0897	00	BNDX3	
0898	00	BNDX3	
0899	00	BNDX3	
089A	00	BNDX3	
089B	00	BNDX3	
089C	00	BNDX3	
089D	00	BNDX3	
089E	00	BNDX3	
089F	00	BNDX3	
08A0	00	BNDX3	
08A1	00	BNDX3	
08A2	00	BNDX3	
08A3	00	BNDX3	
08A4	00	BNDX3	
08A5	00	BNDX3	
08A6	00	BNDX3	
08A7	00	BNDX3	
08A8	00	BNDX3	
08A9	00	BNDX3	
08AA	00	BNDX3	
08AB	00	BNDX3	
08AC	00	BNDX3	
08AD	00	BNDX3	
08AE	00	BNDX3	
08AF	00	BNDX3	
08B0	00	BNDX3	
08B1	00	BNDX3	
08B2	00	BNDX3	
08B3	00	BNDX3	
08B4	00	BNDX3	
08B5	00	BNDX3	
08B6	00	BNDX3	
08B7	00	BNDX3	
08B8	00	BNDX3	
08B9	00	BNDX3	
08BA	00	BNDX3	
08BB	00	BNDX3	
08BC	00	BNDX3	
08BD	00	BNDX3	
08BE	00	BNDX3	
08BF	00	BNDX3	
08C0	00	BNDX3	
08C1	00	BNDX3	
08C2	00	BNDX3	
08C3	00	BNDX3	
08C4	00	BNDX3	
08C5	00	BNDX3	
08C6	00	BNDX3	
08C7	00	BNDX3	
08C8	00	BNDX3	
08C9	00	BNDX3	
08CA	00	BNDX3	
08CB	00	BNDX3	
08CC	00	BNDX3	
08CD	00	BNDX3	

Address	Instruction	Address	Instruction	Address	Instruction	Address	Instruction
0973 24	DFB	174	DFB	09C0 74	DFB	0108	DFB
0974 53	DFB	172	DFB	09C1 72	DFB	0110	DFB
0975 19	DFB	141	DFB	09C4 44	DFB	0090	DFB
0976 A1	DFB	168	DFB	09C5 68	DFB	0013	DFB
0977 00	DFB	168	DFB	09C6 B2	DFB	0008	DFB
0978 C0	DFB	168	DFB	09C7 32	DFB	0046	DFB
0979 1A	DFB	168	DFB	09C8 62	DFB	0000	DFB
097E 5B	DFB	168	DFB	09C9 00	DFB	0000	DFB
097F 58	DFB	168	DFB	09CA 32	DFB	0000	DFB
0980 A5	DFB	168	DFB	09CB 00	DFB	0000	DFB
0981 69	DFB	168	DFB	09CC 1A	DFB	0000	DFB
0982 24	DFB	168	DFB	09CD 1A	DFB	0000	DFB
0983 4E	DFB	168	DFB	09CE 26	DFB	0000	DFB
0984 HE	DFB	168	DFB	09CF 00	DFB	0000	DFB
0985 HE	DFB	168	DFB	09D0 72	DFB	0000	DFB
0986 A3	DFB	168	DFB	09D1 72	DFB	0000	DFB
0987 AD	DFB	168	DFB	09D2 88	DFB	0000	DFB
0988 29	DFB	168	DFB	09D3 08	DFB	0000	DFB
0989 00	DFB	168	DFB	09D4 04	DFB	0000	DFB
098A 7C	DFB	168	DFB	09D5 0A	DFB	0000	DFB
098B 00	DFB	168	DFB	09D6 26	DFB	0000	DFB
098C 15	DFB	168	DFB	09D7 48	DFB	0000	DFB
098D 9C	DFB	168	DFB	09D8 44	DFB	0000	DFB
098E 6D	DFB	168	DFB	09D9 44	DFB	0000	DFB
098F 60	DFB	168	DFB	09DA 44	DFB	0000	DFB
0990 65	DFB	168	DFB	09DB 42	DFB	0000	DFB
0991 62	DFB	168	DFB	09DC C8	DFB	0000	DFB
0992 39	DFB	168	DFB	09DD C8	DFB	0000	DFB
0993 53	DFB	168	DFB	09DE C8	DFB	0000	DFB
0994 54	DFB	168	DFB	09DF C8	DFB	0000	DFB
0995 13	DFB	168	DFB	09E0 C8	DFB	0000	DFB
0996 34	DFB	168	DFB	09E1 C8	DFB	0000	DFB
0997 11	DFB	168	DFB	09E2 C8	DFB	0000	DFB
0998 05	DFB	168	DFB	09E3 C8	DFB	0000	DFB
0999 59	DFB	168	DFB	09E4 C8	DFB	0000	DFB
09A0 23	DFB	168	DFB	09E5 C8	DFB	0000	DFB
09A1 52	DFB	168	DFB	09E6 C8	DFB	0000	DFB
09A2 94	DFB	168	DFB	09E7 C8	DFB	0000	DFB
09A3 83	DFB	168	DFB	09E8 C8	DFB	0000	DFB
09A4 54	DFB	168	DFB	09E9 C8	DFB	0000	DFB
09A5 41	DFB	168	DFB	09EA C8	DFB	0000	DFB
09A6 08	DFB	168	DFB	09EB C8	DFB	0000	DFB
09A7 54	DFB	168	DFB	09EC C8	DFB	0000	DFB
09A8 03	DFB	168	DFB	09ED C8	DFB	0000	DFB
09A9 41	DFB	168	DFB	09EE C8	DFB	0000	DFB
09AA E3	DFB	168	DFB	09EF C8	DFB	0000	DFB
09AB 94	DFB	168	DFB	09F0 C8	DFB	0000	DFB
09AC 00	DFB	168	DFB	09F1 C8	DFB	0000	DFB
09AD 64	DFB	168	DFB	09F2 C8	DFB	0000	DFB
09AE 08	DFB	168	DFB	09F3 C8	DFB	0000	DFB
09AF 04	DFB	168	DFB	09F4 C8	DFB	0000	DFB
09B0 74	DFB	168	DFB	09F5 C8	DFB	0000	DFB
09B1 64	DFB	168	DFB	09F6 C8	DFB	0000	DFB
09B2 28	DFB	168	DFB	09F7 C8	DFB	0000	DFB
09B3 28	DFB	168	DFB	09F8 C8	DFB	0000	DFB
09B4 5E	DFB	168	DFB	09F9 C8	DFB	0000	DFB
09B5 74	DFB	168	DFB	09FA C8	DFB	0000	DFB
09B6 74	DFB	168	DFB	09FB C8	DFB	0000	DFB
09B7 4H	DFB	168	DFB	09FC C8	DFB	0000	DFB
09B8 72	DFB	168	DFB	09FD C8	DFB	0000	DFB
09B9 02	DFB	168	DFB	09FE C8	DFB	0000	DFB
09BA 04	DFB	168	DFB	09FF C8	DFB	0000	DFB
09BB 8H	DFB	168	DFB	0900 00	DFB	0000	DFB
09BC 00	DFB	168	DFB	0901 00	DFB	0000	DFB
09BD 0H	DFB	168	DFB	0902 00	DFB	0000	DFB
09BE 02	DFB	168	DFB	0903 00	DFB	0000	DFB
09BF 02	DFB	168	DFB	0904 00	DFB	0000	DFB
09C0 74	DFB	168	DFB	0905 00	DFB	0000	DFB
09C1 74	DFB	168	DFB	0906 00	DFB	0000	DFB